

FORM PTO-1449		Docket Number (Optional) 81790.0297		Application Number Not assigned	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION		Applicant Daisuke KATO et al.			
(Use several sheets if necessary)		Filing Date September 17, 2003		Group Art Unit Not assigned <i>2818</i>	
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
<i>VN</i>	5,546,349	8/13/96	WATANABE et al.		
<i>VN</i>	5,615,164	3/25/97	KIRIHATA et al.		
<i>VN</i>	5,841,710	11-1998	LARSEN	365	200
<i>VN</i>	6,396,748	05-2002	FUJITA	365	201
FOREIGN PATENT DOCUMENTS					
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)					
<i>VN</i>	Yohji WATANABE et al., "A 286 mm ² 256Mb DRAM with x32 Both-Ends DQ", IEEE Journal of Solid-State Circuits, Vol. 31, No. 4, April 1996, pp 567-574.				
<i>VIET Q. NGUYEN PRIMARY EXAMINER</i>					
EXAMINER	<i>V. Nguyen</i>	DATE CONSIDERED <i>11/2/09</i>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.					